Course Description Form

| 1. Course Name | | | | | | | |
|---|--|------------------------|--------|---|---------------------|-----------------|--|
| Digital Electronics | | | | | | | |
| 2. Course Code | | | | | | | |
| WBM-41-06 | | | | | | | |
| 3. Sen | 3. Semester/Year | | | | | | |
| Quart | erly | | | | | | |
| 4. Dat | te of prep | aration of this desc | ripti | on | | | |
| 2024 | 9\23 | | | | | | |
| 5. Ava | ailable at | tendance forms | | | | | |
| Week | ly (theor | etical) | | | | | |
| 6. Nu | mber of c | credit hours (total) / | / tota | l number of units | | | |
| 60 Th | eoretical | Hours / 3 Units | | | | | |
| 7. Cou | 7. Course Administrator Name | | | | | | |
| Name | e: Dr :Hu | ssien kaream | | | | | |
| Email: h | nussien.karea | am@uowa.iq | | | | | |
| 8. Coi | arse Obje | ectives | | | • .• • • • | 1 • 6 | |
| Course Objectives: | | | | • This course description provides a brief | | | |
| | | | | summary of the n | nost important | characteristics | |
| | | | | the course and the learning outcomes expected | | | |
| | | | | made the most of | the available 1 | earning | |
| | | | | opportunities It r | ult available i | to the program | |
| | | | | description | nust be miked | to the program | |
| 1 Teaching and learning strategies | | | | | | | |
| 1. reaching and rearing strategies Strategy The student's ability to analyze, apply and arrange knowledge so that | | | | | | | |
| Dirace | 75 7 | ne can impose assu | mntie | ons and interpretat | tion as well as o | describe | |
| | | solutions. The abili | tv to | learn simple and c | leep in explori | ng knowledge | |
| | | and focusing on the | e appl | lication of knowle | dge to solve existi | ng | |
| | problems Discrimination that the test increases the student's motivation | | | | | | |
| | 1 | owards study and i | s not | a means of punisl | hment for him. | | |
| | | 2 | | 1 | | | |
| | | | | | | | |
| 2 | . Course | Structure | | | | | |
| The | TT | Required | | Learning | Evaluation | | |
| week | Hours | Learning | Unit | t or subject name | method | method | |
| 1-3 | 12 | Knowledge of | Intr | oduction to | Lectures / | Exams / | |
| 1.5 | hours | circuit design | Dig | ital | Assignments | Assignments | |
| | | Special Electronic | Ele | ctronics: Number | / Open | / Ouick | |
| | | MUX and Pal | Svs | tems and Codes | Discussion / | Exams / | |
| | | DUX | | | Real-life | Seminars | |
| | | | | | Keal-life | Seminars | |

| | | represent the same number in counting systems, (decimal, octal, hexadecimal, binary) as well as converting the number from one counting system to another | | Examples | and Discussions |
|-----|-------------|--|---|---|--|
| 4-6 | 12 hours | Learn logic gates (truth table, symbol, and action) As well as learning Boolean algebra and DeMorgan's theorem | Boolean Algebra and Logic Gates | Lectures / Assignments / Open Discussion / Real-life Examples Practical connectivity | Exams / Assignments / Quick Exams / Seminars and Discussions |
| 7 | 4 hours | Rules of methods of writing logical equations in both forms (SOP, POS) | Rules of methods of writing logical equations in both forms (SOP, POS | Lectures / Assignments / Open Discussion / Real-life Examples Practical connectivity | Exams / Assignments / Quick Exams / Seminars and Discussions |
| 8-9 | 8 hours | Karnaugh maps (2-variables, 3- variables, 4- and 5- variables Don't care | Karnaugh maps (2- variables, 3- variables, 4- and 5- variables Don't care | Lectures / Assignments / Open Discussion / Real-life Examples Practical connectivity | Exams / Assignments / Quick Exams / Seminars and Discussions |
| 10 | 4 hours | Arithmetic operations (adder, parallel binary adder) | Arithmetic operations (adder, parallel binary adder) half and full | Lectures / Assignments / Open Discussion / | Exams / Assignments / Quick Exams / |

| 1 | 1 | 1 10 1 0 11 | 1 1 1 | | D 11'C | | |
|--------------------------------|-------------------------------------|---|----------------------------------|--|--------------------------------------|-------------|--|
| | | half and full | adder | | Real-life | Seminars | |
| | | adder | | | Examples | and | |
| | | | | | Practical | Discussions | |
| | | | | | connectivity | | |
| | | | | | Lectures / | Exams / | |
| | | Design of Combinational Logic Circuit | | | Assignments | Assignments | |
| 11 | 4 hours | | ~ | | / Open | / Quick | |
| | | | Combi | national | Discussion / | Exams / | |
| | | | Logic (| Circuit | Real-life | Seminars | |
| | | | | | Examples | and | |
| | | | | | Practical | Discussions | |
| | | | | | connectivity | | |
| | 12 | | Encoder and | | Lectures / | Exams / | |
| | | Design of | | | Assignments | Assignments | |
| | | | | | / Open | / Quick | |
| 12- | | Decoder | Decode | r, | Discussion / | Exams / | |
| 14 | | Multiplexer and Demultiplexer | Multiplexer and Demultiplexer | | Real-life | Seminars | |
| | | | | | Examples | and | |
| | | | | | Practical | Discussions | |
| | | | | | connectivity | | |
| | 4 | Design Comparator and code conversions | Comparator and code conversions | | Lectures / | Exams / | |
| | | | | | Assignments | Assignments | |
| | | | | | / Open | / Quick | |
| 15 | | | | | Discussion / | Exams / | |
| 15 | | | | | Real-life | Seminars | |
| | | | | | Examples | and | |
| | | | | | Practical | Discussions | |
| | | | | | connectivity | | |
| 3 | . Course | Evaluation | | | | | |
| Distrib | outing the s | core out of 100 accord | ding to the | tasks assigned t | to the student such | n as daily | |
| prepar | ation, daily | , oral, monthly, writte | en exams, | reports etc | | - | |
| 4 | . Learning | g and Teaching Re | sources | | | | |
| | | | | | Digital logic and computer Design by | | |
| I-Required textbooks | | | | Morris Mano | | | |
| 2-Main references (sources) | | | | Digital Fundamental by Thomas L. Floyd | | | |
| a) | a) Recommended books and references | | | | Scientific journals in the specialty | | |
| (scientific journals reports) | | | | Selentine jot | sinuis in the sp | conney | |
| (serentine journuis, reports,) | | | | Websites specialized in studying the | | | |
| b) | Electroni | c references, websi | ites, | material | | | |
| | | | | | | | |

